

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"10/066019"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L2	8	"6236524".pn. "6137832".pn. "6005892".pn. "4727578".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L3	16	switch adj matrix with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L4	0	input adj ports and output adj ports and eqializ\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L5	3419	input adj ports and output adj ports and equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L6	166	input adj ports with output adj ports with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L7	14	input adj ports with output adj ports with equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L8	0	input adj ports with output adj ports with equaliz\$5 with loss and swith with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L9	0	input adj ports with output adj ports with equaliz\$5 with loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L10	5	input adj ports same output adj ports same equaliz\$5 same loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L11	56	switch with matrix and equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L12	62585	hasegawa.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L13	2	"5191431".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L14	352	asymmetrical with compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L15	49	asymmetrical adj compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L16	226	access with request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L17	73	access adj request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L18	46	access adj request near memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L19	4	access adj request near memory near throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L20	3	"10091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L21	1	"10/091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L22	25	switch near matrix with equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L23	0	switch\$3 near matrix with equaliz\$6 with jitter with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L24	4	switch\$3 near matrix with equaliz\$6 and jitter and loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L25	12	switch near matrix with equaliz\$6 and induc\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L26	0	equaliz\$6 with inductance with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L27	0	equaliz\$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L28	0	equaliz\$6 same inductor same resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L29	1	equaliz\$6 and inductor and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L30	2	equaliz\$6 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L31	3	switch\$4 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L32	80866	switch\$4 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L33	135	equaliz\$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L34	58	equaliz\$6 with inductor with resistor and switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L35	11	equaliz\$6 with inductor with resistor with switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L36	1	equaliz\$6 with inductor with resistor and switch\$4 and skin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L37	1561	375/229	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L38	595	switch near matrix and equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L39	12	L37 and L38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L40	1942	375/232	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L41	4	L38 and L40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L42	3183	jitter with pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L43	326	jitter with pll with vco	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L44	9	jitter with pll with vco with measurement	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L45	0	input adj ports and output adj ports and equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L46	3419	input adj ports and output adj ports and equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L47	166	input adj ports with output adj ports with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L48	0	input adj ports with output adj ports with equaliz\$5 with loss and swith with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L49	0	input adj ports with output adj ports with equaliz\$5 with loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L50	62585	hasegawa.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L51	352	asymmetrical with compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L52	226	access with request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L53	73	access adj request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L54	0	switch\$3 near matrix with equaliz\$6 with jitter with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L55	4	switch\$3 near matrix with equaliz\$6 and jitter and loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L56	0	equaliz\$6 with inductance with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L57	0	equaliz\$6 with inductor with ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L58	0	equaliz\$6 same inductor same ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L59	80866	switch\$4 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L60	1561	375/229	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L61	595	switch near matrix and equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L62	1942	375/232	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L63	1	"10/066019"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L64	1	"10/091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L65	1	equaliz\$6 and inductor and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L66	1	equaliz\$6 with inductor with resistor and switch\$4 and skin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L67	8	"6236524".pn. "6137832".pn. "6005892".pn. "4727578".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L68	16	switch adj matrix with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L69	14	input adj ports with output adj ports with equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L70	5	input adj ports same output adj ports same equaliz\$5 same loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L71	56	switch with matrix and equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L72	2	"5191431".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L73	49	asymmetrical adj compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L74	46	access adj request near memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L75	4	access adj request near memory near throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L76	3	"10091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L77	25	switch near matrix with equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L78	12	switch near matrix with equaliz\$6 and induc\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L79	2	equaliz\$6 and induct\$4 and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L80	3	switch\$4 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28

L81	58	equaliz\$6 with inductor with resistor and switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L82	11	equaliz\$6 with inductor with resistor with switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L83	12	L60 and L61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L84	4	L61 and L62	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28
L85	135	equaliz\$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 19:28